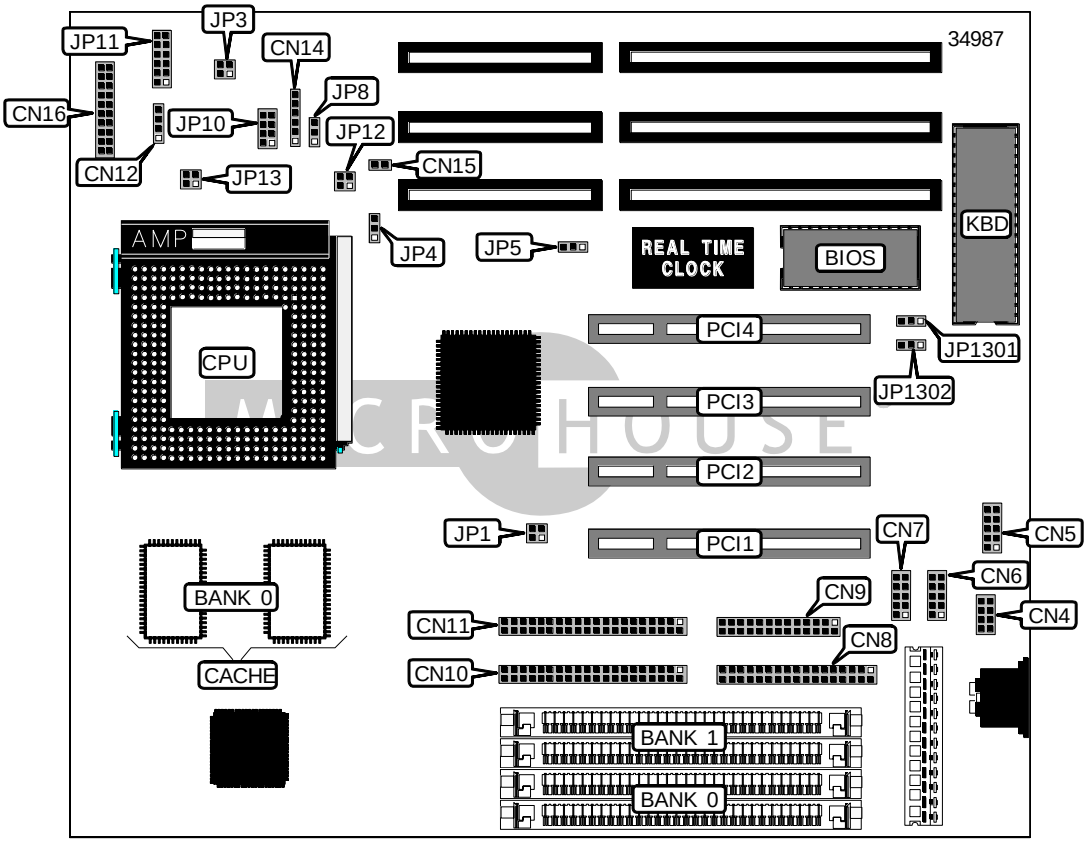
ACER, INC.
AP53 (Rev. 3.0)

Processor	Pentium/Pentium MMX/AMK5/AMK6/CX 6x86
Processor Speed	75/90/100/120/133/150/166/200/233MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI
Dimensions	250mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, USB connector, IR connector, serial ports (2)
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
PS/2 mouse interface	CN4	IR connector	CN14
USB connector	CN5	CPU fan connector	CN15
Serial port 2	CN6	Keylock	CN16 Pins 1&2
Serial port 1	CN7	Power LED	CN16 Pins 3-5
Floppy drive interface	CN8	Speaker	CN16 Pins 7-10
Parallel port	CN9	Green PC LED	CN16 Pins 12&13
IDE interface 1	CN10	Suspend switch	CN16 Pins 15-17
IDE interface 2	CN11	Reset switch	CN16 Pins 19&20
IDE LED connector	CN12	32-bit PCI slots	PCI1-4

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í PS/2 mouse IRQ12 enabled	JP4	Closed
PS/2 mouse IRQ12 disabled	JP4	Open
í CMOS memory normal operation	JP5	Pins 1 & 2 closed
CMOS memory clear	JP5	Pins 2 & 3 closed
í On board I/O controller enabled	JP8	Pins 1 & 2 closed
On board I/O controller disabled	JP8	Pins 2 & 3 closed
í I/O voltage set at 3.43V	JP12	Pins 1 & 2 closed
I/O Voltage set at 3.52V	JP12	Pins 3 & 4 closed
Factory configured - do not alter	JP1301	Pins 2 & 3 closed
Factory configured - do not alter	JP1302	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
4MB	(2) 512K x 36	None
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 1M x 36	(2) 8M x 36

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DRAM CONFIGURATION (CONT.)		
Size	**Bank 0**	**Bank 1**
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 1M x 36	(2) 16M x 36
144MB	(2) 2M x 36	(2) 16M x 36
160MB	(2) 4M x 36	(2) 16M x 36
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	None
264MB	(2) 1M x 36	(2) 32M x 36
272MB	(2) 2M x 36	(2) 32M x 36
288MB	(2) 4M x 36	(2) 32M x 36
320MB	(2) 8M x 36	(2) 32M x 36
384MB	(2) 16M x 36	(2) 32M x 36
512MB	(2) 32M x 36	(2) 32M x 36

CACHE CONFIGURATION	
Size	**Bank 0**
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (INTEL)				
Speed	**Clock Speed**	**Multiplier**	**JP10**	**JP1**
75MHz	50MHz	1.5x	1 & 2, 3 & 4	1 & 2, 3 & 4
90MHz	60MHz	1.5x	1 & 2, 3 & 4	1 & 2
100MHz	66MHz	1.5x	1 & 2	3 & 4
120MHz	60MHz	2x	3 & 4, 5 & 6	1 & 2
133MHz	66MHz	2x	3 & 4, 5 & 6	3 & 4
150MHz	60 MHz	2.5x	5 & 6, 7 & 8	1 & 2
166MHz	66MHz	2.5x	5 & 6, 7 & 8	3 & 4
200MHz	66MHz	3x	1 & 2, 7 & 8	3 & 4
Note: Pins designated should be in the closed position				

CPU SPEED SELECTION (INTEL MMX)				
Speed	**Clock Speed**	**Multiplier**	**JP10**	**JP1**
150MHz	60MHz	2.5x	5 & 6, 7 & 8	1 & 2
166MHz	66MHz	2.5x	5 & 6, 7 & 8	3 & 4
200MHz	66MHz	3x	1 & 2, 7 & 8	3 & 4
233MHz	66MHz	3.5x	1 & 2, 3 & 4	3 & 4
Note: Pins designated should be in the closed position				

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CPU SPEED SELECTION (CX 6X86)				
Speed	**Clock Speed**	**Multiplier**	**JP10**	**JP1**
120MHz	50MHz	2x	3 & 4, 5 & 6	1 & 2, 3 & 4
133MHz	55MHz	2x	3 & 4, 5 & 6	All open
150MHz	60MHz	2x	3 & 4, 5 & 6	1 & 2
166MHz	66MHz	2x	3 & 4, 5 & 6	3 & 4
Note: Pins designated should be in the closed position				

CPU SPEED SELECTION (AMK5)				
Speed	**Clock Speed**	**Multiplier**	**JP10**	**JP1**
75MHz	50MHz	1.5x	1 & 2, 3 & 4	1 & 2, 3 & 4
90MHz	60MHz	1.5x	1 & 2, 3 & 4	1 & 2
100MHz	66MHz	1.5x	1 & 2, 3 & 4	3 & 4
120MHz	60MHz	1.5x	1 & 2, 3 & 4	1 & 2
133MHz	66MHz	1.5x	1 & 2, 3 & 4	3 & 4
166MHz	66MHz	1.75x	5 & 6, 7 & 8	3 & 4
Note: Pins designated should be in the closed position				

CPU SPEED SELECTION (AMK6)				
Speed	**Clock Speed**	**Multiplier**	**JP10**	**JP1**
166MHz	66MHz	2.5x	5 & 5, 7 & 8	3 & 4
200MHz	66MHz	3x	1 & 2, 7 & 8	3 & 4
233MHz	66MHz	3.5x	1 & 2, 3 & 4	3 & 4
Note: Pins designated should be in the closed position				

CPU VOLTAGE SELECTION (SINGLE)	
Voltage	**JP11**
2.5V	Pins 5 & 6 closed
2.8V	Pins 9 & 10 closed
2.9V	Pins 11 & 12 closed
3.2V	Pins 7 & 8 closed
3.45V	Pins 1 & 2 closed
3.52V	Pins 3 & 4 closed

CPU VOLTAGE SELECTION (DUAL)				
Voltage	**JP11**	**JP12**	**JP3**	**JP13**
2.8V	9 & 10	1 & 2	All open	1 & 2, 3 & 4
2.9V	11 & 12	1 & 2	All open	1 & 2, 3 & 4
3.2V	7 & 8	1 & 2	All open	1 & 2, 3 & 4
3.43V	1 & 2	1 & 2	1 & 2, 3 & 4	All open
3.52V	3 & 4	1 & 2	1 & 2, 3 & 4	All open
Note: Pins designated should be in the closed position				